

**REMARKS**

Claims 1, 3, 5-12, and 14-18 are pending. The Examiner rejects claims 1-18 [sic] under 35 U.S.C. § 102(b) as being anticipated by Roohparvar (U.S. Pat. No. 5,526,364).

Applicants amend claims 1, 3, 8, 10, 14 and 15, cancel claims 17 and 18, and add claims 19 and 20. Claims 1, 3, 5-12, 14-16, 19, and 20 remain in the application.

**Claim Rejections – 35 USC § 102(b)**

The Examiner rejects claims 1-18 under 35 U.S.C. 102(b) as being anticipated by Roohparvar. Applicants respectfully disagree and amend claims 1, 3, 8 and 10 for reasons not related to patentability.

Claim 1, as amended, recites *a mode set circuit electrically connected with the at least one pad to set input and output mode responsive to the at least one external signal*. Independent claims 3 and 10 contain similar limitations. Applicants point out the meaning of *input and output mode* from the specification as relating to “the number of data items simultaneously input to and output from the memory device.” Examples are given showing output mode set to x4, x8, and x16, among others. Therefore, the output modes can also be thought of as the data batch size for the semiconductor memory.

Roohparvar discloses an apparatus for generating test-mode signals for memory which uses both hardware and software protection schemes. Roohparvar makes no mention of setting input and output mode in this manner or in any manner. Claims 1, 3 and 10 are therefore allowable over Roohparvar. Since dependent claims necessarily contain the limitations of claims from which they depend, claims 5-9, 11, 12, 14-16, 19 and 20 are also in condition for allowance.

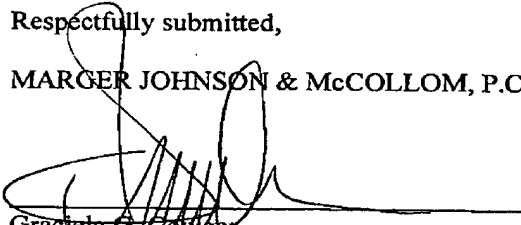
Claims 19 and 20 are essentially previously cancelled claims 4 and 13.

**Conclusion**

For the foregoing reasons, Applicants request reconsideration and allowance of all amended claims. Applicants encourage the Examiner telephone the undersigned at (503) 222-3613 if it appears that an interview would be helpful in advancing the case.

Respectfully submitted,

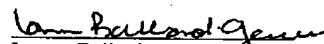
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PAGE 10 OF 10

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